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# Study on the antimony tin oxide as a hole injection layer for polymer light emitting diodes

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#### article info abstract

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#### 1. Introduction

The fabrication of devices such as organic light emitting devices [\[1](#page-5-0)-7], organic thin film transistors [8-[11\]](#page-5-0) and polymer solar cells [\[12](#page-5-0)–18] as next-generation devices has attracted a great deal of attention. In fact, there have been numerous studies on this topic. The πconjugated polymers which are used in the active layer of these devices have good stability, good mechanical properties and excellent electrooptical properties. Because they enable the solution process to be used, it is possible to fabricate low-cost and large-area devices by a simple method such as spin coating instead of vacuum deposition. In particular, screen printing [\[19\]](#page-5-0), ink-jet printing [20–[23\],](#page-5-0) micro contact printing [\[24,25\]](#page-5-0) and the stamping method [\[26,27\]](#page-5-0) can be applied to polymer light emitting diodes (PLEDs) which use a polymer as an emitting layer, because their solubility is good in organic solvents. As a result, low-cost, large-area, flexible and lightweight devices can be produced.

In the case of PLEDs, electrons and holes are injected from the anode and cathode respectively, recombine in the emitting polymers, fall into the ground state and emit light. To improve the efficiency of PLEDs, the balance of charge injection and transport from each electrode is an important factor [\[28](#page-5-0)–32]. For balanced charge injection, multilayer architectures, for example, the introduction of both hole- and electron-transporting layers, is required [\[33,34\].](#page-5-0) A number of hole injection/transporting polymers have been reported. Poly (3,4 ethylenedioxythiophene) blended with poly (styrenesulfonate) (PEDOT:PSS) is one of the hole transport layer (HTL) materials, which is extensively used as an interfacial layer to improve the hole transporting properties in most organic devices. Although PEDOT:PSS

Poly(3,4-ethylenedioxythiophene):poly(styrenesulfonate) is commonly used as a hole transfer layer in polymer light emitting diodes (PLEDs). However, Indium tin oxide transparent electrodes are corroded by poly(styrenesulfonate) and the erupted indium diffuses into the active layer, which in turn decreases the brightness, efficiency and lifetime of the device. In this study, therefore, antimony tin oxide (ATO) was introduced as a hole injection layer (HIL) in PLEDs. The work function and pH of ATO were −5.1 eV and ~7.5, respectively. When annealed at 200 °C, high conductivity  $(-0.18 S/cm)$  was observed, which represents good HIL characteristics. Here, the maximum luminance (26,114 cd/m<sup>2</sup>) and maximum efficiency (1.55 cd/A) of the PLEDs were increased by 33% and 20% respectively. Their stability improved as well.

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shows excellent performance, it has some disadvantages, one of which is the formation of an unstable interface between indium tin oxide (ITO) and PEDOT-PSS. Moreover, the PSS in PEDOT:PSS has strongly acidic functional groups which cause the corrosion of ITO and the diffusion of indium into the emitting layer [\[35\]](#page-5-0).

Recently, hole transporting materials designed to replace PEDOT: PSS have been reported. Niu et al. and Lim et al. introduced thermal cross-linkable hole transporting layers such as thermal curable triphenyldiamine and triphenylamine containing trifluorovinyl ether [\[36,37\]](#page-5-0). Using these materials, it was possible to fabricate PLEDs with a multi-layer structure. A wide range of similar hole injection and transporting materials have been developed, but they have the disadvantage of complicated synthesis processes and high production costs. Therefore, in the current study, we fabricated PLEDs by introducing a hole injection layer (HIL).

In this study, we fabricated PLEDs by using antimony tin oxide (ATO), which is neutral and has high conductivity, as the HIL. UV–visible spectroscopy was used to evaluate the transmittance and solvent resistance of the ATO thin film and atomic force microscopy (AFM) was used to observe the surface morphology of the film. Finally, the structure of the fabricated device was optimized by controlling the thickness and thermal treatment temperature of the ATO thin film and thickness of PEDOT:PSS.

#### 2. Materials and measurements

#### 2.1. Materials

Poly [2-methoxy-5-(2-ethylhexyloxy)-1,4-phenylenevinylene] (MEH-PPV) was used for the emitting layer and it was purchased

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<span id="page-1-0"></span>

Fig. 1. Structure of PLEDs in this study.

from American Dye Source. The antimony tin oxide solution was purchased from Keeling & Walker and PEDOT:PSS (AI 4083) was purchased from Baytron.

#### 2.2. Measurements

All of the thin films were fabricated using a GMC2 spin coater (Gensys, Korea) and their thickness was measured using an alpha step 500 surface profiler (KLA-Tencor). The absorbance and transmittance of the ATO film were measured using an Agilent 8453 UV–visible spectrometer (HP Agilent) and the conductivity was measured with a 4-point probe station (MST 6000C). Also, the electro-optical properties of the fabricated device were characterized using a Keithley 2400 source meter unit (Keithley) and a PR 670 spectra scan spectroradiometer (Photo Research), while the surface morphology of the thin film was measured using AFM (PSIA XE-150) and transmission electron microscopy (TEM, JEOL JEM-2010).

#### 3. Experiments

### 3.1. ITO cleaning and film formation of antimony tin oxide

To clean the ITO glass (10  $\Omega$ /sq, Samsung Corning), sonication was performed using detergent (Alconox® in deionized (DI) water, 10%), acetone, isopropyl alcohol and DI water, in the order listed, for 20 min each.

The moisture was removed by blowing thoroughly with  $N_2$  gas. In order to ensure the complete removal of all of the remaining water,



Fig. 2. Transmittance and thickness of ATO thin films by spin coating speed.



Fig. 3. TEM (operating at 200 kV) image of ATO thin film.

the ITO was baked on a hot plate for 10 min at 120 °C. For the hydrophilic treatment of the glass surface, it was cleaned for 10 min in a UVO cleaner. To measure the thickness and transmittance of the ATO film spin-coating speed, it was spin-coated at speeds of 1000– 4000 rpm and annealed at 200 °C for an hour.

To measure the solvent resistance, the ATO solution which was filtered with a 5 μm polytetrafluoroethylene (PTFE) syringe filter on the cleaned ITO glass was coated at 3000 rpm. Then, it was annealed at 200 °C for an hour to remove the residual solvent. As a result, a 160 nm thick thin film was obtained. The fabricated ATO thin film was rinsed with DI water, chlorobenzene, chloroform and methyl chloride. In addition, the conductivity of the ATO film was measured using a 4-point probe station.

#### 3.2. Fabrication of PLEDs

The PEDOT:PSS and ATO solutions were blended for 24 h after being filtered with a 0.45 μm polyvinylidene fluoride syringe filter and 5 μm PTFE syringe filter, respectively. After dissolving the MEH-PPV, which is applied to the emitting layer in chlorobenzene (0.5 wt.%) and shaking it for 24 h, it was filtered with a 5 μm PTFE syringe filter. To fabricate the PLEDs, ATO solutions were spin-coated (120 nm thick) on the clean patterned ITO glass and annealed at 100 °C–300 °C for an hour. PEDOT:PSS was spin-coated with a range of thicknesses of 30–75 nm and annealed at 120 °C for 20 min. In the case of the orange polymers, they were coated with a thickness of 80 nm and annealed at 90 °C for an hour to remove the residual solvents. The metal electrodes were thermally deposited in a high-vacuum chamber (1.3×10<sup>-4</sup> Pa or less) in the order of BaF<sub>2</sub> (0.1 Å/s, 2 nm), Ba (0.2 Å/s, 2 nm) and Al (5 Å/s, 200 nm). To protect the organic layer and the electrode layer from  $H<sub>2</sub>O$  and  $O<sub>2</sub>$ , a getter was attached to the inside of the glass cover to



Fig. 4. Absorption spectrums of ATO thin film.

encapsulate the device. The composition of the fabricated device is shown in [Fig. 1.](#page-1-0)

#### 4. Results and discussion

#### 4.1. Thin film of antimony tin oxide

[Fig. 2](#page-1-0) shows the transmittance as a function of the thickness of the ATO thin film measured by UV–visible spectroscopy. At a thickness of 120 nm, 96% (at 550 nm, visible region) transmittance was observed. In the range of thicknesses from 120 nm to 360 nm, 90% or higher transmittance was detected. In the used ATO solution,  $SnO<sub>2</sub>$  and  $Sb<sub>2</sub>O<sub>3</sub>$  (nano-sized particle metal oxides) are colloidally dispersed in water. The ATO solution was spin-coated on ITO glass followed by thermal treatment at 200 °C. Then, the residual solvent was removed. Consequently producing net-structured channels through which the carriers are able to move. Because of these net-structured channels, the ATO film has high transmittance [\(Fig. 3\)](#page-1-0). It was expected that the optical absorption and light scattering would be reduced when using the ATO buffer layer as a HIL.

To measure the solvent resistance, the ATO thin film coated on the ITO glass was rinsed with DI water, chlorobenzene, methyl chloride or chloroform. [Fig. 4](#page-1-0) shows the measured absorbance of the ATO thin films rinsed by the various solvents. According to [Fig. 4,](#page-1-0) almost no change of the absorbance was observed. The  $SnO<sub>2</sub>$  and  $Sb<sub>2</sub>O<sub>3</sub>$  nanoparticles cohered to each other during the thermal annealing process. Therefore, the physical coherence between the ITO and ATO film was improved. As a result, it was found that the ATO thin film showed strong resistance to the solvents. It was expected that surface mixing would not take place when PEDOT:PSS (water base) or the polymer (organic solvent base) was coated on the ATO thin film.

#### 4.2. Characterization of PLEDs

[Fig. 1](#page-1-0) shows the structure of the PLEDs. In addition, the brightness– voltage and efficiency–current density characteristics of the PLEDs fabricated with various thicknesses (120–360 nm) of the ATO thin film and various annealing temperatures (100–300 °C) are shown in Figs. 5 and 6, respectively. The turn on voltage was 2 V without change of the thickness of ATO and thermal annealing temperature altogether. The best characteristics of the PLEDs were observed when they were annealed at 200 °C.

In addition, to optimize the device structure, the brightness–voltage and efficiency–current density characteristics as a function of the thickness of PEDOT:PSS (HTL) are shown in Fig. 7. The turn on voltage did not



Fig. 5. Brightness-voltage graph by various annealing temperature of ATO thin film (device configuration: ITO/ATO (120-360 nm)/PEDOT:PSS (40 nm)/polymer (80 nm)/BaF<sub>2</sub> (2 nm)/Ba (2 nm)/Al (200 nm)).



Fig. 6. Efficiency–current density graph by various annealing temperature of ATO thin film (device configuration: ITO/ATO (120–360 nm)/PEDOT:PSS (40 nm)/polymer(80 nm)/ BaF2 (2 nm)/Ba (2 nm)/Al (200 nm)).

vary from 2 V and the best characteristics were observed at a thickness of 40 nm.

[Table 1](#page-3-0) shows the optimized structure and characteristics of each device. In the case of Device I in which PEDOT:PSS only was used as the HTL, the maximum brightness and maximum efficiency recorded were 19,585  $\text{cd/m}^2$  and 1.30  $\text{cd/A}$ , respectively. However, an improvement of the characteristics was observed in Device II and Device III, in which an ATO/PEDOT:PSS structure was fabricated using different annealing temperatures. In particular, in Device III in which the thickness and annealing temperature of the ATO thin film and PEDOT:PSS were optimized, the maximum brightness (26,114 cd/ m<sup>2</sup> ) and maximum efficiency (1.55 cd/A) increased by 33% and 20%, respectively, compared to those of Device I, because ATO facilitated the hole injection from the anode to the PEDOT:PSS layer. The work function of ITO and highest occupied molecular orbital level of PEDOT:PSS were found to be 4.7 eV and 5.2 eV, respectively. The ATO layer (5.1 eV) has the effect of lowering the hole-injection barrier [\(Fig. 8\)](#page-3-0). In addition, in terms of the conductivity of the ATO film, which were annealed at 100, 200 and 300 °C, values of 0.03, 0.18 and 2.2 S/cm were observed, respectively, which were greater than that of PEDOT:PSS which exhibited values in the range of 10<sup>-3</sup> to 10<sup>-4</sup> S/cm. Because of the high conductivity, therefore, it was possible for hole injection to the emission layer to take place quickly. Therefore, the luminance efficiency was enhanced by increasing the probability of recombination with electrons.

To compare the carrier mobility of Device I and Device III, space charge limited current (SCLC) measurements were used. [Fig. 9](#page-3-0)



Fig. 7. Brightness-voltage and efficiency-current density graph (inset) by various thickness of PEDOT:PSS (device configuration: ITO/ATO (120 nm)/PEDOT:PSS (35– 75 nm)/polymer (80 nm)/BaF2 (2 nm)/Ba (2 nm)/Al (200 nm)).

<span id="page-3-0"></span>



ITO  $(170 \text{ nm})/I$ –IV/orange polymer  $(80 \text{ nm})/BaF_2$   $(2 \text{ nm})/Ba$   $(2 \text{ nm})/Al$   $(200 \text{ nm})$ .

shows the J–V characteristics of a log–log plot and inset shows the J–V curves. SCLC can be described by

$$
J = \frac{9}{8} \epsilon \epsilon_0 \mu \frac{V^2}{L^3}
$$
 (1)

where E is the electric field,  $\varepsilon$  and  $\varepsilon_0$  are the relative dielectric constant and the permittivity of the free space, respectively, and L is the thickness of the organic layer. The hole mobilities of the devices were calculated from Eq. (1) using the J–V curves of the hole-only devices with the configuration ITO/with or without ATO(120 nm)/PEDOT:PSS(40 nm)/ MEH-PPV(80 nm)/MoO<sub>3</sub>(30 nm)/Al(100 nm). The hole mobility of the hole-only device without ATO layer was calculated to be  $4.43 \times$  $10^{-3}$  cm<sup>2</sup>/V s, whereas the device with ATO layer as the hole injection layer had a value as high as  $5.91 \times 10^{-3}$  cm<sup>2</sup>/V s. The hole mobility was 33% higher than that of the device without ATO layer. Because of the high hole mobility of the device with ATO layer, therefore, it was possible for higher maximum brightness and maximum efficiency increased by 33% and 20%, respectively.

Fig. 10 shows the result of accelerated operation-lifetime test of PELDs. In Device I, the extrapolated half-lifetime under the brightness of 10,000 cd/ $m<sup>2</sup>$  was 9.3 h. However, half-lifetime of Device III was 14.3 h and 53% was improved, compared to Device I. Two possible reasons may explain this phenomenon. Firstly, the conductivity of HTL was improved by the introduction of the ATO layer, the holes were efficiently injected from ITO to the light-emitting layer. Therefore, because the heat where it is generated in the device was decreased, the lifetime was increased. Secondly, pH of ATO was the neutral with the ~6.5 and was introduced between ITO and PEDOT:PSS layer. The ATO did not erode ITO because of being the neutral. And it prevented the corrosion by PEDOT:PSS.

With increasing annealing temperature, the conductivity of the ATO film increased. However, the characteristics of Device IV were lower than those of Device III, because of the change in the conductivity caused by the increase in the annealing temperature. The sheet



Fig. 8. Energy-band diagram of device.

resistance of the ATO film which was annealed at 100 °C was very high, viz.  $6 \times 10^5 \Omega$ /sq. However, that of the ATO film which was annealed at 300 °C decreased somewhat to  $9 \times 10^3 \Omega$ /sq. In our previous study, when the ATO film was annealed at 500 °C, its sheet resistance decreased to 30  $\Omega$ /sq, which is sufficiently low for the film to be used as an anode [\[38\]](#page-5-0). However, the conductivity of the ATO film (~6500 S/ cm) is lower than that of ITO (~8000 S/cm). Therefore, when it was annealed at 300 °C, the ATO film coated on ITO showed the characteristics of an anode, not those of the buffer layer. Because of low conductivity of ATO film, its characteristics are lower than those of Device III.

[Fig. 11](#page-4-0) shows the EL spectra of the Devices (From I to IV). All of the devices showed the same CIE coordinates (0.57, 0.42). In other words, no color change was observed when ATO was introduced with the HIL. In addition, the color purity of Device III (full width at half maximum (FWHM) of 110 nm) was greater than that of Device I (FWHM of 118 nm).

[Fig. 12](#page-4-0) shows the surface morphology images of ITO, ITO/ATO, and ITO/ATO/PEDOT:PSS. ITO showed a good morphology with a root



Fig. 9. J–V characteristics of a log–log plot and the J–V curves of hole only devices (inset).



Fig. 10. Accelerated operation-lifetime test of devices.

<span id="page-4-0"></span>

Fig. 11. EL spectrum of devices.



mean square (RMS) of 0.302 nm. When ATO was coated on ITO, on the contrary, the morphology was poorer with an RMS of 7.92 nm. When PEDOT:PSS was coated on ATO, however, the roughness was improved, with an RMS value of 3.36 nm being observed.

### 5. Conclusion

In this study, the maximum brightness, maximum efficiency and stability of PLEDs were improved by preventing ITO from being corroded by PSS (strong acid) through the development of a HIL/HTL structure (ITO/ATO/PEDOT:PSS) in which ATO was applied as a buffer layer between and ITO and PEDOT:PSS. It was confirmed that ATO is appropriate as an interlayer between the ITO anode and PEDOT:PSS, thanks to its strong solvent resistance properties against DI water and organic solvents such as chlorobenzene, chloroform and methyl chloride. When the ATO thin film was annealed at 200 °C, high





<span id="page-5-0"></span>conductivity (0.18 S/cm) was observed. In addition, the maximum brightness and maximum efficiency were increased by 33% and 20%, respectively, because the hole transfer from the anode to the emitting layer was facilitated. Furthermore, the color purity has improved, with an FWHM value of 110 nm being obtained. It was also confirmed that the device with the ATO thin film was more stable than that in which only PEDOT:PSS was used, because no degradation occurred in it despite the increase in the applied voltage.

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